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1 [HFS: a performance-oriented flexible file system based on building-t](#)  
 Orran Krieger, Michael Stumm  
 August 1997 **Transactions on Computer Systems (TOCS)**, Volume 15 I:

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The Hurricane File System (HFS) is designed for (potentially large-scale the principle that, in order to maximize performance for applications wit

**Keywords:** customization, data partitioning, data replication, flexibility

2 [A survey of processors with explicit multithreading](#)

Theo Ungerer, Borut Robič, Jurij Silc

March 2003

**Computing Surveys (CSUR)**, Volume 35 Issue 1

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Hardware multithreading is becoming a generally applied technique in t  
 processors are announced by industry or already into production in the

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3 [The hybrid scheduling framework for virtual machine systems](#)

Chuliang Weng, Zhigang Wang, Minglu Li, Xinda Lu

March 2009

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The virtualization technology makes it feasible that multiple guest opera  
 virtual machine monitor that dynamically maps the virtual CPU of virtua

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**Key words:** hybrid scheduling, scheduling strategy, virtualization

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 Michal Wegiel, Chandra Krintz

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Parallel and concurrent garbage collectors are increasingly employed by scalability, as multi-core architectures and multi-threaded applications t commonly ...

**Key words:** compaction, concurrent, parallel, virtual memory


Also published in:

March 2008 **SIGOPS Operating Systems Review** Volume 42 Issue 2

March 2008 **SIGPLAN Notices** Volume 43 Issue 3

March 2008 **SIGARCH Computer Architecture News** Volume 36 Issue 1

#### 5 [Leveraging cache coherence in active memory systems](#)

 Daehyun Kim, Mainak Chaudhuri, Mark Heinrich

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Active memory systems help processors overcome the memory wall wh either active memory elements that perform data parallel computations controller ...

**Key words:** active memory, address re-mapping, cache coherence

#### 6 [Automatic customization of device drivers for IP-cores used with ass](#)

 Andrea Acquaviva, Nicola Bombieri, Franco Fummi, Sara Vinco

October 2009 **CODES+ ISSS '09: Proceedings of the 7th IEEE/ACM intern system synthesis**

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Plugging an IP core into an embedded platform implies the generation c protocol from one side and with the CPU organization (i.e., single proce




**Key words:** device driver design, embedded systems, hardware-softwa

-  [XMem: type-safe, transparent, shared memory for cross-runtime con](#)  
 Michal Wegiel, Chandra Krintz  
 June 2008 **PLDI '08: Proceedings of the 2008 ACM SIGPLAN conferen**  
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Developers commonly build contemporary enterprise applications using architect them to comprise multiple tiers, such as a web container, appl increasingly ...

**Keywords:** class loading, garbage collection, interprocess communicati synchronization, transparent, type-safe

Also published in:  
 May 2008 **SIGPLAN Notices** Volume 43 Issue 6

- 8 [Computation spreading: employing hardware migration to specialize](#)  
 Koushik Chakraborty, Philip M. Wells, Gurindar S. Sohi  
 November 2006 **ASPLOS-XII: Proceedings of the 12th international confer**  
 operating systems  
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**Bibliometrics:** Downloads (6 Weeks): 21, Downloads (12 Months): 127, Downl

In canonical parallel processing, the operating system (OS) assigns a pr application. Since different threads from the same application often can

**Keywords:** cache locality, dynamic specialization

Also published in:  
 October 2006 **SIGARCH Computer Architecture News** Volume 34 Issue 5  
 October 2006 **SIGOPS Operating Systems Review** Volume 40 Issue 5  
 November 2006 **SIGPLAN Notices** Volume 41 Issue 11

- 9 [Nonclairvoyant scheduling to minimize the total flow time on single a](#)  
 Luca Becchetti, Stefano Leonardi  
 July 2004 **Journal of the ACM (JACM)** , Volume 51 Issue 4  
**Publisher:** ACM  [Request Permissions](#)  
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**Bibliometrics:** Downloads (6 Weeks): 5, Downloads (12 Months): 52, Downlo

Scheduling a sequence of jobs released over time when the processing problem in CPU scheduling in time sharing operating systems. A widely

**Keywords:** Probabilistic analysis, flow time, multilevel feedback, rando


Impact of job mix on optimizations for space sharing schedulers

Jaspal Subhlok, Thomas Gross, Takashi Suzuoka


November 1996 **Supercomputing '96: Proceedings of the 1996 ACM/IEEE****Publisher:** IEEE Computer SocietyFull text available:  Pdf (134.91 KB)Additional Information: [full citation](#)**Bibliometrics:** Downloads (6 Weeks): 1, Downloads (12 Months): 13, Download

This paper addresses job scheduling for parallel supercomputers. Moder requesting up to  $n$  nodes. If less than  $n$  nodes are requested, multiple j

**Keywords:** backfilling, job characteristics, job scheduling, scheduling o workloads

**11** The effect of seance communication on multiprocessing systems Avi Mendelson, Freddy Gabbay

May 2001

**Transactions on Computer Systems (TOCS)**, Volume 19 I:**Publisher:** ACM Full text available:  Pdf (337.24 KB)Additional Information: [full citation](#)**Bibliometrics:** Downloads (6 Weeks): 0, Downloads (12 Months): 13, Download


This paper introduces the seance communication phenomenon and anal communication is an unnecessary coherency-related activity that is ass

**Keywords:** cache coherency protocols, multicache systems, performan


**12** Implementing AUTOSAR scheduling and resource management on i

Florian Kluge, Chenglong Yu, Jörg Mische, Sascha Uhrig, Theo Ungerer

April 2009


**SCOPES '09: Proceedings of th 12th International Worksho**Full text available:  Pdf (284.60 KB)Additional Information: [full citation](#)**Bibliometrics:** Downloads (6 Weeks): 9, Downloads (12 Months): 81, Download

The AUTOSAR specification provides a common standard for software d is based on the concept of single-threaded processors. Recent trends in

**13** Efficient, portable implementation of asynchronous multi-place progr Ganesh Bikshandi, Jose G. Castanos, Sreedhar B. Kodali, V. Krishna Nandi


Pradeep Varma, Tong Wen

February 2009


**PPoPP '09: Proceedings of the 14th ACM SIGPLAN symposi****Publisher:** ACM Full text available:  Pdf (462.47 KB)Additional Information: [full citation](#)**Bibliometrics:** Downloads (6 Weeks): 19, Downloads (12 Months): 174, Downl

The X10 programming language is organized around the notion of place data), partitioned global address space (PGAS), and asynchronous com expressive ...


**Keywords:** apgas, asynchrony, compiler, fft, hpc, hpc challenge, pgas,

**14** [High performance computing: the software challenges](#) Michael A. Bauer


July 2007

**PASCO '07: Proceedings of the 2007 international workshop****Publisher:** ACM  [Request Permissions](#)Full text available:  Pdf (160.41 KB)Additional Information: [full citation](#)**Bibliometrics:** Downloads (6 Weeks): 16, Downloads (12 Months): 121, Downloads (36 Months): 121

In the past decade we have seen significant advances in the reliability of processors, disks and network devices. Processors, in particular, have increased

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The Campus Information Technology and Educational Services group is at Urbana-Champaign. The CITES Help Desk provides IT support for faculty and staff.

**Keywords:** intel mac, parallels, virtualization**16** [Hardware and software architectures for the CELL processor](#) Peter Hofstee, Michael Day


September 2005

**CODES+ISSS '05: Proceedings of the 3rd IEEE/ACM/IFIP system synthesis****Publisher:** ACMFull text available:  Pdf (111.04 KB)Additional Information: [full citation](#)**Bibliometrics:** Downloads (6 Weeks): 1, Downloads (12 Months): 54, Downloads (36 Months): 54


The Cell processor is a first instance of a new family of processors intended for use in game systems (PlayStation3TM), a variety of other consumer electronics, and servers.

**17** [Memory management challenges in the power-aware computing era](#) Avi Mendelson

June 2006


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Process technology has been driving the computer architecture industry. In the past, micro-architectures were focused on achieving best performance, usually at the cost of power consumption.

**18** [Scaling NFS through RDMA for cluster computing](#) Dov Cohen, Jeff Decker, Noah Fischer, Helen Y. Chen, Jackie H. ChenNovember 2006 **SC '06: Proceedings of the 2006 ACM/IEEE conference on Supercomputing****Publisher:** ACM

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Parallel applications combine the power of a large number of commodity data structures throughout the memory of all participating clustered cor

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David R. Naugler

April 2008


**Journal of Computing Sciences in Colleges**, Volume 23 Iss**Publisher:** Consortium for Computing Sciences in CollegesFull text available:  [Pdf \(21.49 KB\)](#)[Additional Information: full citation](#)**Bibliometrics:** Downloads (6 Weeks): 10, Downloads (12 Months): 39, Downlo

Erlang is a general purpose programming language of the functional fan  
It is dynamically typed, makes extensive use of pattern matching, and i

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Karen A. Frenkel

December 1986

**Communications of the ACM**, Volume 29 Issue 12**Publisher:** ACM  [Request Permissions](#)Full text available:  [Pdf \(267.90 KB\)](#)[Additional Information: full citation](#)**Bibliometrics:** Downloads (6 Weeks): 5, Downloads (12 Months): 61, Download

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